

Amendments to Claims

This listing of claims will replace all prior versions and listings of claims in the application:

Listing of Claims:

What is claimed is:

1. (Currently amended) A method of operation comprising:
 - powering a hardware element of a device with a power supply of the device;
 - operating the hardware element at a first power consumption level while AC is present at the power supply;
 - monitoring for absence of AC to the power supply;
 - generating a signal to indicate AC failure on detection of absence of AC to the power supply, the monitoring and generating occurring while operating the hardware element at the first power consumption level; and
 - in response, throttling the hardware element to operate at a second power consumption level that is a reduced power consumption level than the first power consumption level.
2. (Original) The method of claim 1, wherein the monitoring and generating are performed by the power supply.
3. (Original) The method of claim 1, wherein
 - the hardware element operates at a first clock frequency when operating at the first power consumption level; and
 - the throttling of the hardware element comprises switching the hardware element to operate at a second clock frequency slower than the first clock frequency.
4. (Original) The method of claim 1, wherein
 - the hardware element operates at a first voltage when operating at the first power consumption level; and

the throttling of the hardware element comprises switching the hardware element to operate at a second voltage lower than the first voltage.

5. (Original) The method of claim 1, wherein the hardware element comprises a processor and the throttling of the hardware element comprises periodically interrupting a processor clock.

6. (Original) The method of claim 1, wherein the hardware element comprises a selected one of a processor and a chipset.

7. (Original) The method of claim 1, wherein the method further comprises
waiting for a period of time; and
initiating a process to suspend the ~~apparatus~~ device to memory, if AC remains absent to the power supply after waiting for the period of time.

8. (Original) The method of claim 7, wherein the method further comprises canceling the wait if AC returns during the waiting period.

9. (Original) The method of claim 1, wherein
the hardware element comprises a processor; and
the throttling comprises a chipset in response to the signal, signaling the processor to switch from operating at the first power level of consumption to the second power level of consumption.

10. (Currently amended) A method of operation comprising:
monitoring for re-presence of AC to a power supply of a device after an earlier absence of AC to the power supply;
generating a signal to indicate the presence of AC on detection of re-presence of AC to the power supply; and

throttling a hardware element ~~to switch~~ to operate at a first power consumption level from operating at a second power consumption level in response to the signal, the second power consumption level being a reduced power consumption level than the first power consumption level.

11. (Previously presented) The method of claim 10, wherein the monitoring and generating are performed by the power supply.

12. (Previously presented) The method of claim 10, wherein

the hardware element operates at a first clock frequency when operating at the first power consumption level, and at a second clock frequency when operating at the second power consumption level, the first clock frequency being faster than the second clock frequency; and

the throttling of the hardware element comprises switching the hardware element from operating at the second clock frequency back to operating at the first clock frequency.

13. (Previously presented) The method of claim 10, wherein

the hardware element operates at a first voltage when operating at the first power consumption level, and at a second voltage when operating at the second power consumption level, the first voltage being higher than the second voltage; and

the throttling of the hardware element comprises switching the hardware element from operating at the second voltage to operating at the first voltage.

14. (Previously presented) The method of claim 10, wherein the hardware element comprises a processor, and the throttling comprises ceasing interruption of a processor clock.

15. (Previously presented) The method of claim 10, wherein

the hardware element comprises a processor; and

the throttling comprises a chipset in response to the signal, signaling the processor to switch to operate at the first power consumption level, from operating at the second power consumption level.

16.-25. (Cancelled)